

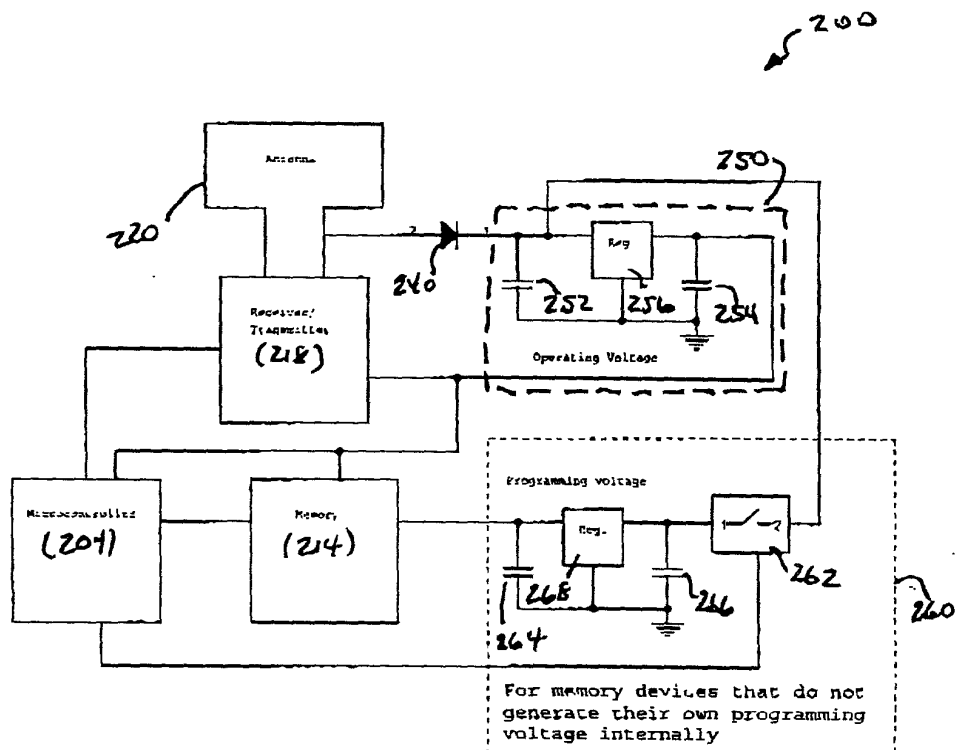
FIGURE 2

FIG. 20 99/04/95





The diagram illustrates a memory device system (200) connected to a microprocessor (204) and memory (214). The system includes a receiver/transmitter (218) and an address/data bus (220). The memory device (250) is shown with internal components like a regulator (Reg.) and capacitors (252, 256, 254). A programming voltage source (260) is also depicted, featuring a switch (262) and a regulator (Reg.) with capacitors (264, 266, 268). A note specifies: "For memory devices that do not generate their own programming voltage internally".



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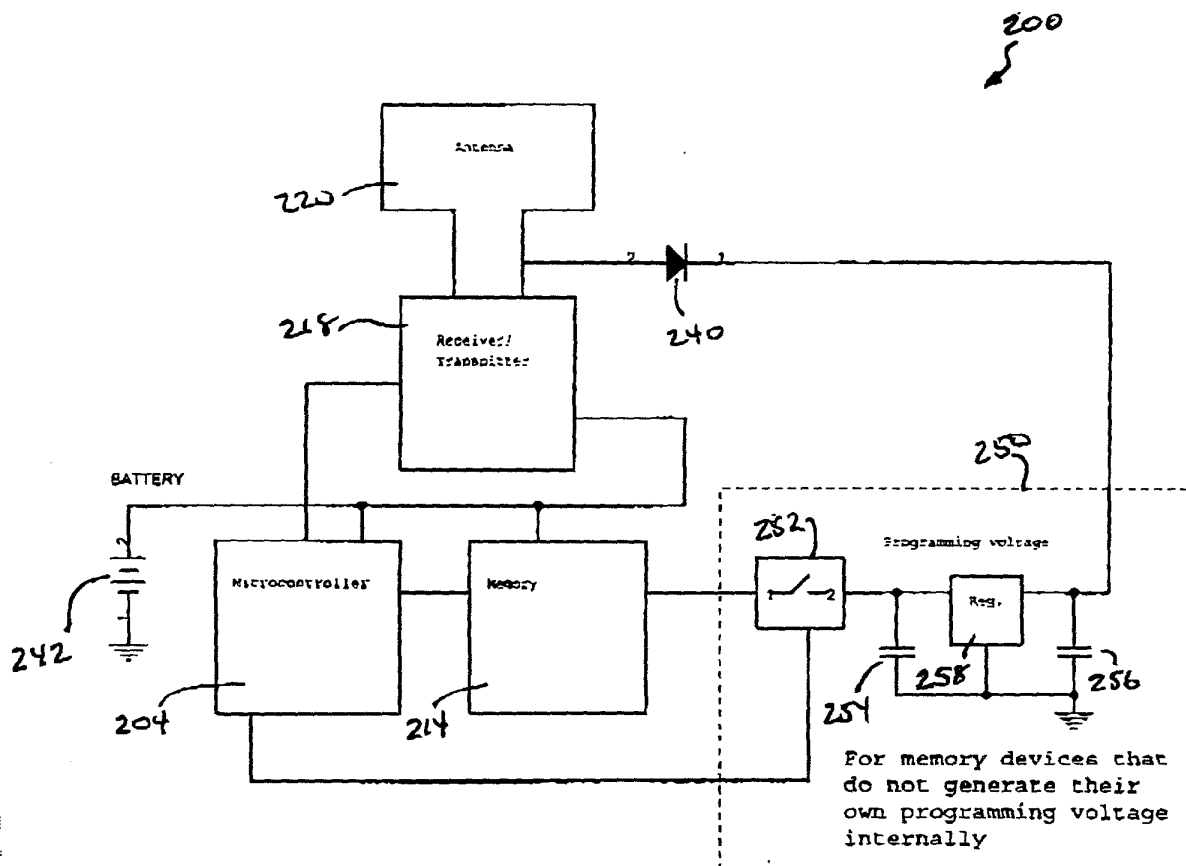


FIGURE 6

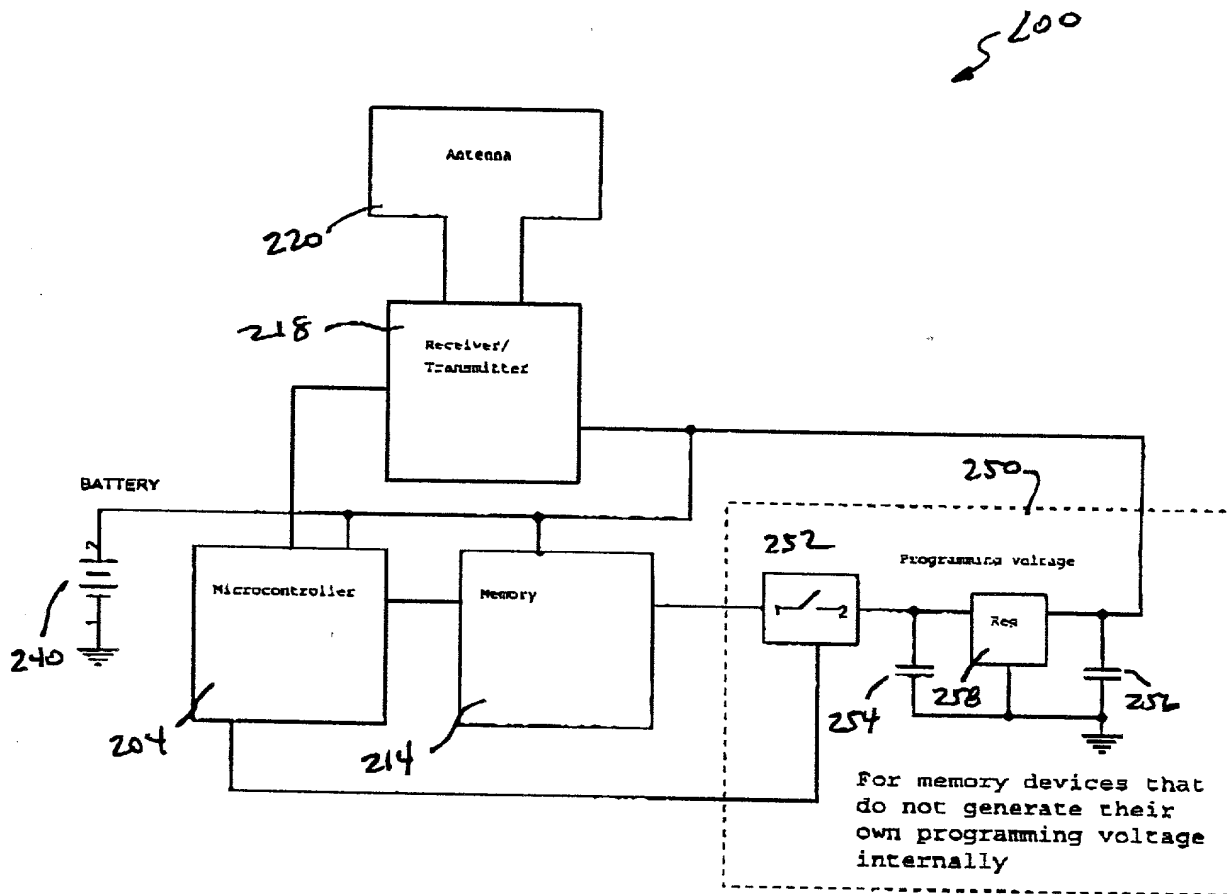


FIGURE 7